

Notice of References Cited	Application/Control No. 09/697,429	Applicant(s)/Patent Under Reexamination TANI, TAKENOBU	
	Examiner Pierre-Michel Bataille	Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0062479	05-2002	Takata, Toshio	717/168
	B	US-6,588,010	07-2003	Ogata, Hitoshi	717/169
	C	US-5,790,860	08-1998	Wetmore et al.	717/122
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Dual Indirect RAM/ROM Jumptable for Firmware Update", IBM Technical Disclosure Bulletin, vol. 31, No. 1, Jun. 1988, pp. 294-298.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.